

# Historie Firmware<sup>v001</sup>

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<b>Datum:</b>	26.11.2012	<b>Version (Historie):</b>	1.6
<b>Betreff:</b>	FW V3.60 for Speed7 CPU's	<b>Erstellt:</b>	Karsten Schmidt

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Effective immediately, the firmware v3.60 for the Speed7 CPUs are available and can be download under [www.vipa.de/de/service-support/downloads/firmware](http://www.vipa.de/de/service-support/downloads/firmware).  
The firmware is compatible with their predecessors.

Further information and downloads can be found at [www.vipa.de](http://www.vipa.de)

## Changes for CPUs

1. Fixed a problem with FESTO valve island
2. When the diagnostics buffer MMC command read is now also registered with the FW version.
3. Global data communication in connection with OB35 no longer breaks.
4. No S7prog file will be created with mmc command "Save\_Project" if plc is deleted
5. All Speed7 CPU'S can also configure with the TIA Portal. In standard Speed7 CPUs 315-2AG, 317-2AJ, 314-6CF, 315-and 317-4NE 4NE these are the CPU's with ID 13 or 03
6. The PN, EtherCAT and the CPU's with ID 13 or 03 now OB 33 and OB34 can be configured directly and not only via SPEEDbus GSD file.
7. PWM function for C-CPU implements
8. Frequency measurement for C-CPU implements
9. New LIB FX000020 for SFC and SFB VIPA in the TIA Portal
10. Diagnosable output modules now operate correctly.